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PATENT ABSTRACTS OF JAPAN

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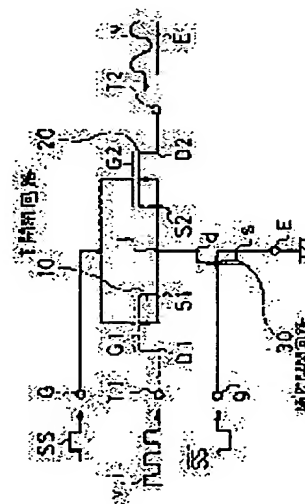
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(54) ANALOG SWITCH CIRCUIT

(57)Abstract:

PURPOSE: To improve switch-off isolation and inter-channel isolation to facilitate integrating a circuit by inserting an auxiliary switching circuit between a reference potential point and the mutual connection point between a pair of MOS transistors TRs of a main switching circuit and allowing the auxiliary switching circuit to perform the switching operation opposite to that of the main switching circuit.

CONSTITUTION: Sources S1 and S2 of MOS TRs 10 and 20 of the main switching circuit are internally connected to a substrate, and input/output terminals T1 and T2 are led out from drains D1 and D2 of both MOS TRs. The auxiliary switching circuit consists of one N-channel MOS TR 30 and is inserted between a reference potential point E as the earth point and a mutual connection point J between sources S1 and S2 of both MOS TRs of the main switching circuit. The gate of the TR 30 is led out as an auxiliary gate terminal (g) and receives an auxiliary signal ss' of a switching command. TRs 10 and 20 and the TR 30 perform switching operations opposite to each other; and when the main switching circuit is opened, the mutual connection point J is connected to the reference potential point E through the TR 30, and both channels are completely isolated by the reference potential point interposed between them.



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